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# Fax

<b>To:</b>	Examiner Aditya S. Bhat	<b>From:</b>	Timothy N. Trop
<b>Company:</b>	US Patent Office	<b>Date:</b>	December 2, 2005
<b>Fax:</b>	(571) 273-8300	<b>Pages:</b>	10 (including cover sheet)
<b>Your Re:</b>	10/660,310	<b>Our Re:</b>	ITL 1029US

☐ Urgent   ☐ For Review   ☐ Please Comment   ☐ Please Reply   ☐ Confirm Receipt

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

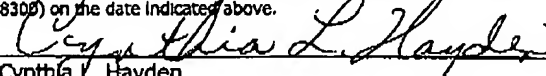
In re Applicant:	§	Art Unit:	2863
Richard L. Coulson et al.	§		
Serial No.:	§	Examiner:	Aditya S. Bhat
10/660,310	§		
Filed:	§	Docket:	ITL.1029US
September 11, 2003	§	P16765	
For: Adaptive Cache Algorithm for	§	Assignee:	Intel Corporation
Temperature Sensitive Memory	§		

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REPLY TO FINAL REJECTION**

Sir:

In response to the final rejection mailed October 25, 2005, please amend the above-referenced patent application as follows:

Date of Deposit: December 2, 2005  
I hereby certify that this document is being facsimile transmitted to the United States Patent and Trademark Office (Fax No. 571/273-8300) on the date indicated above.  
  
Cynthia L. Hayden

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